

In the Claims:

1. (currently amended) A radio frequency output power control system for use in communication systems that use a modulation scheme having a non-constant amplitude envelope, said power control system comprising:

a power amplifier having a power amplifier input for receiving an input signal with a non-constant amplitude envelope, a power control input for receiving a power control signal, and a power amplifier output for providing an amplified output signal;

a track and hold circuit for tracking a measured reference power signal that is representative of a modulation of the input signal; and

subtraction means for ~~subtracting both a feedback signal that is representative of a modulation of the amplifier output signal and~~

subtracting an output of said track and hold circuit from said measured reference power signal that is representative of the modulation of the input signal, and

subtracting a feedback signal that is representative of a modulation of the amplifier output signal from said measured reference power signal

to provide a power control signal that is coupled to the power control input, such that said power control signal is responsive to the feedback signal, the output of said track and hold circuit, and the measured reference power signal.

2. (original) The radio frequency output power control system as claimed in claim 1, wherein said track and hold circuit and said measured reference power signal are coupled to the input signal via a logarithmic power detect unit.

3. (original) The radio frequency output power control system as claimed in claim 1, wherein the track and hold circuit is responsive to a $HOLD_{on}$ signal.
4. (original) The radio frequency output power control system as claimed in claim 1, wherein said input signal is an IF output signal provided by a transmitter unit.
5. (original) The radio frequency output power control system as claimed in claim 1, wherein said input signal comprises baseband reference outputs from a transmitter unit.
6. (original) The radio frequency output power control system as claimed in claim 5, wherein said system further includes a pair of squaring units.
7. (original) The radio frequency output power control system as claimed in claim 1, wherein said track and hold circuit is coupled to said power amplifier via an error amplifier.
8. (original) The radio frequency output power control system as claimed in claim 1, wherein said system is employed to control power for a modulated signal with non-constant envelope.
9. (cancelled)
10. (original) The radio frequency output power control system as claimed in claim 1, wherein said system is further responsive to a TX_{Ramp} signal.
11. (original) The radio frequency output power control system as claimed in claim 1, wherein said system further includes a feedback logarithmic power detect unit.

12. (original) A radio frequency output power control system for use in communication systems that use a modulation scheme having a non-constant amplitude envelope, said power control system comprising:

a power amplifier having an input to receive an input signal with a non-constant amplitude envelope, a power control input for receiving a power control signal, and an output for providing an amplified output signal;

a track and hold circuit for tracking a reference signal;

a first combiner for providing a difference between said reference signal and an output signal of said track and hold circuit, and

a second combiner for providing a difference between a feedback signal and an output of said first combiner, wherein said power control signal is responsive to the difference between the feedback signal and the output of the first combiner.

13. (original) The radio frequency output power control system as claimed in claim 12, wherein said control system further includes a third combiner for providing a sum of a TX_{Ramp} signal and an output of said second combiner, wherein said power control signal is responsive to the sum of the TX_{Ramp} signal and the output of the second combiner.

14. (original) The radio frequency output power control system as claimed in claim 13, wherein said first combiner, said second combiner, and said third combiner are all provided in a single combiner unit.

15. (currently amended) A radio frequency output power control system comprising:

a power amplifier coupled to an input signal for providing an amplifier output signal;

a reference logarithmic unit coupled to a reference signal that is representative of a modulation of the input signal;

a track and hold circuit coupled to said reference signal;

a feedback logarithmic unit coupled to a feedback signal that is representative of a modulation of the amplifier output signal; and

combiner means for providing a corrective signal to said power amplifier responsive to said reference signal, an output signal from said track and hold circuit, and said feedback signal by

subtracting ~~said feedback signal and~~ said output of said track and hold circuit from said reference signal that is representative of a modulation of the input signal,
and

subtracting said feedback signal from said reference signal

to provide the corrective signal that is coupled to the power amplifier.

16. (original) The radio frequency output power control system as claimed in claim 15, wherein said reference signal includes a modulated RF signal.

17. (original) The radio frequency output power control system as claimed in claim 15, wherein said reference signal includes a modulated IF signal.

18. (original) The radio frequency output power control system as claimed in claim 15, wherein said reference signal includes baseband I and Q signals.

19. (original) The radio frequency output power control system as claimed in claim 18, wherein said system is employed to control power for a modulated signal with non-constant envelope.

20. (original) The radio frequency output power control system as claimed in claim 15, wherein said combiner means is further responsive to a TX_{Ramp} signal.

21. (original) The radio frequency output power control system as claimed in claim 15, wherein said track and hold circuit is responsive to a $HOLD_{on}$ signal.